



The Abdus Salam
**International Centre
for Theoretical Physics**
www.ictp.it



Conference and Workshop on Advanced Reconfigurable Instrumentation for Scientific Applications

**14 - 25 November 2016
Selangor – Malaysia**

The *Abdus Salam International Centre for Theoretical Physics* (ICTP, Italy) and the *University Kebangsaan Malaysia* (UKM, Malaysia) will jointly organize the **Asian Regional Conference and Workshop on Advanced Reconfigurable Instrumentation for Scientific Applications**, to be held at the UKM, **Bangi, Selangor, Malaysia**, from **14 to 25 November 2016**.

The first three days of the activity will be dedicated to the **2016 International Conference on Advances in Electrical, Electronic and Systems Engineering (ICAEESE 2016)** to be held at Putrajaya Marriot Hotel, **Putrajaya, Malaysia**, from **14 to 16 November 2016**. The theme of the conference is "*Technologies for Society's Well-being*". *Prof. Kajita Takaaki, 2015 Nobel Prize in Physics, and Prof. Fernando Quevedo, Director of ICTP, will be Keynote Speakers at the Conference*. Specific information about the Conference can be found at <http://www.icaeese2016.org/>

BRIEF DESCRIPTION OF THE WORKSHOP

This Workshop is mainly intended as a training activity on modern FPGA-based Programmable System-on-Chip (SoC) with emphasis on scientific and academic applications. The purpose of the Workshop is to provide key know-how for the implementation of complete digital systems using low-cost high-performance reconfigurable hardware platforms. New generation of SoC technology relies on embedded processing based on the combination of general purpose processors with traditional FPGA integrated in a single chip. This means that the processor and the FPGA can be complementary used for what they do best, achieving unprecedented performance.

The main topics of the Workshop will include the fundamentals of:

- Modern Digital Design and Digital Arithmetic
- FPGA and VHDL for Modeling, Simulation and Logic Synthesis
- Embedded C Language Programming and Dual-Core ARM Cortex Processor
- SoC Design Methodology
- Advanced SoC Trends
- Hands-on Training and Experimentation

Scientists, technologists, teachers and students from South and Southeast Asian countries may apply for participation.

GRANTS

A limited number of grants are available to support the travel and living expenses of selected participants, with priority given to participants working in a developing country and who are at the early stages of their career.

There is no registration fee to attend the Workshop.

Selected participants of the Workshop will automatically enjoy the waiver of the IC AE ESE 2016 registration fees.

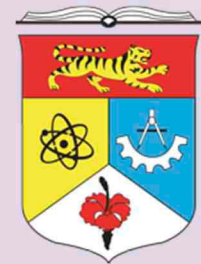
HOW TO APPLY FOR PARTICIPATION

The Online Application Form can be accessed at the *Workshop website*:

<http://indico.ictp.it/event/7598>

The website has comprehensive, step-by-step instructions to guide you on how to fill out and submit the application form **not later than 07 August 2016**.

Recommendation letters are not mandatory, but may help you in the admission process.



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**APPLICATION
DEADLINE**

07 August 2016

