

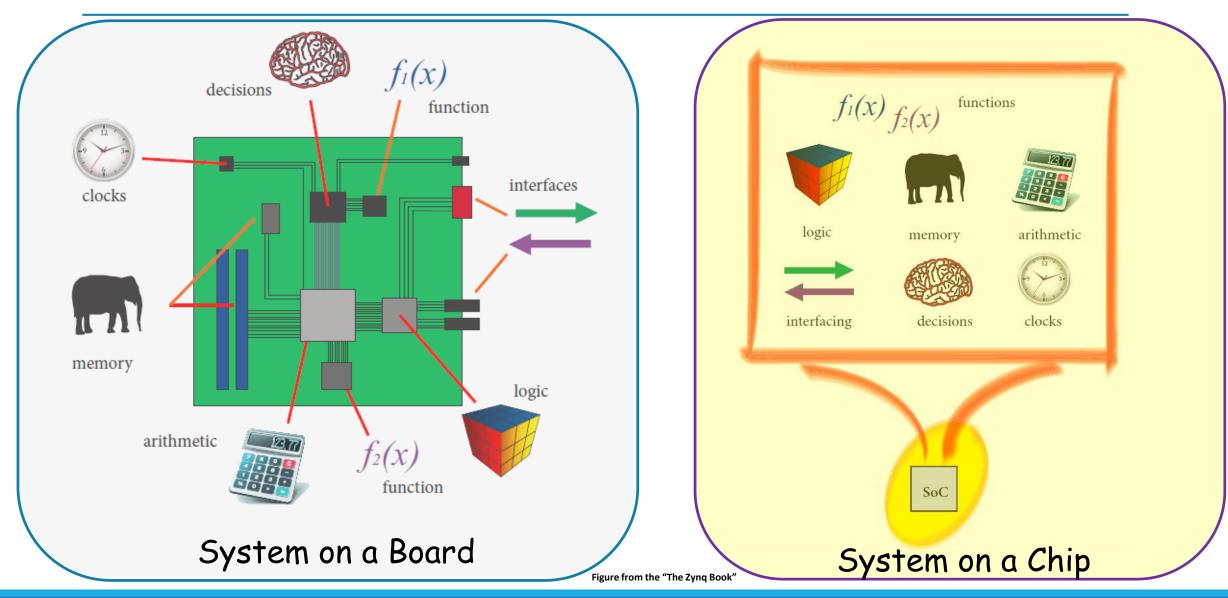
# System on a Chip (SoC)

Cristian Sisterna

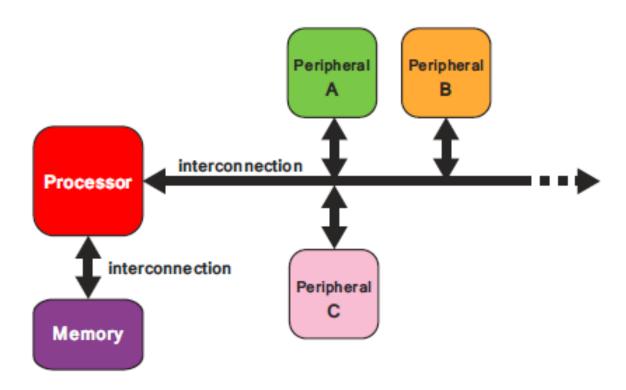
Universidad Nacional San Juan Argentina

SoC ICTP - IAEA 1

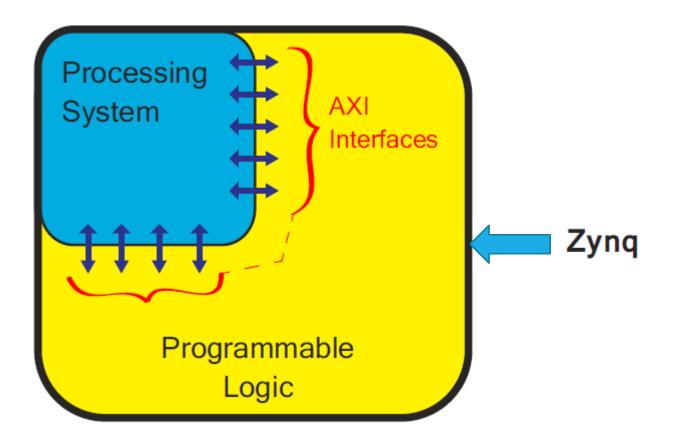
## System on Chip with Zynq



# A Simple View of an Embedded SoC



## A Simple View of the Zynq SoC



#### Software System, Hardware System and Zynq

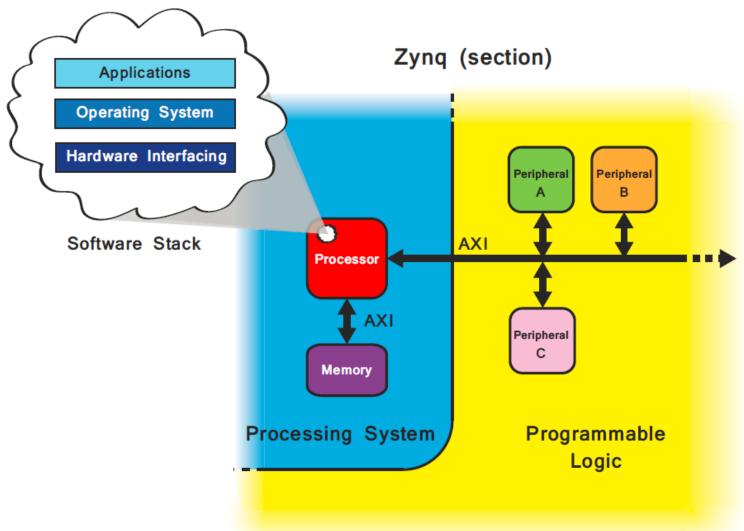
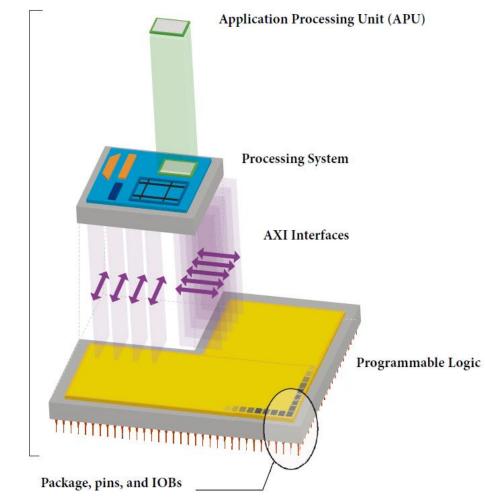


Figure from the "The Zynq Book"

# Architectural View of the Zynq



# SoC Design Flow

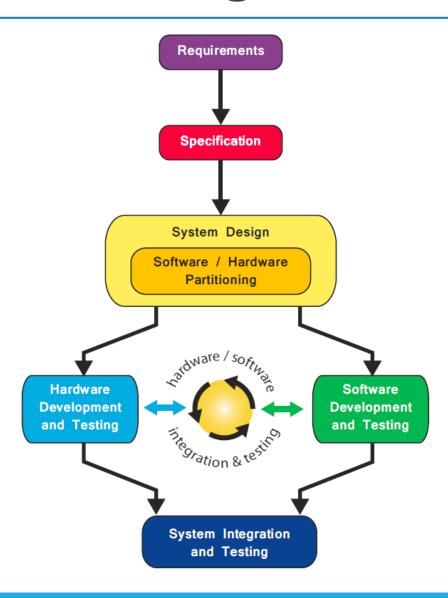
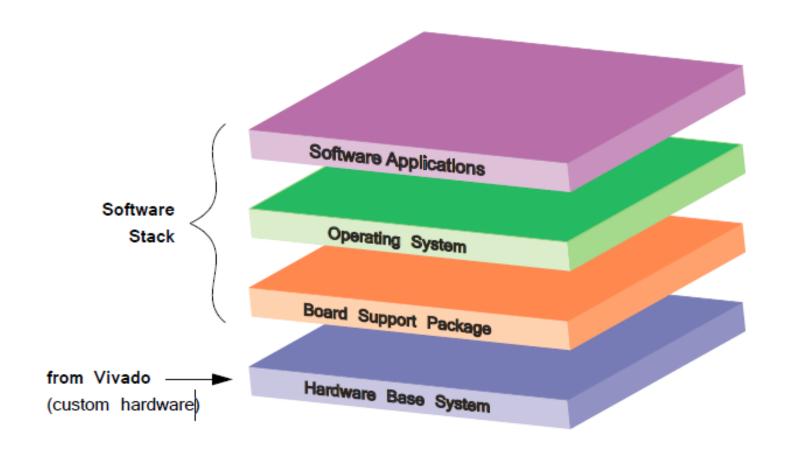


Figure from the "The Zynq Book"

## Hardware and Software Layers in a SoC



## IP Availability for SoC Designs

