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Joint ICTP-IAEA School on Zynq-7000 SoC and its Applications for Nuclear and Related Instrumentation | (smr 3143)

Monday 04 September 2017

High Speed Data Acquisition - Chairperson: Maria Liz Crespo - Leonardo Building - Computer Lab (Leonardo Building) (09:00-17:30)

| time | title | presenter |
|-------|--|--------------|
| 09:00 | High speed ADC and DAC boards. Generic Block Design. | HEINZ RONGEN |
| 10:30 | Coffee break | |
| 11:00 | Creating IP blocks for high speed data acquisition. | HEINZ RONGEN |
| 12:30 | Lunch break | |
| 14:00 | Hands-on activities: Arbitrary Function-Generator IP | |
| 15:30 | Coffee break | |
| 16:00 | Hands-on activities: Digital Oscilloscope IP | |