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# Advanced Workshop on FPGA-based Systems-On-Chip for Scientific Instrumentation and Reconfigurable Computing | (smr 3249)

## Wednesday 05 December 2018

**- Adriatico Guest House - Giambiagi Lecture Hall (09:00-18:30)**

time	title	presenter
09:00	SoC FPGA Design Verification and Timing Concepts	N. ABDALLAH
10:00	Coffee break	
10:30	Reconfigurable Virtual Instrumentation (RVI) based on SoC	A. CICCUTTIN
11:30	Lab activities with Tutors	TUTORS
12:30	Lunch break	
14:00	From FPGA-based Reconfigurable Systems to Autonomic Heterogeneous Computing Systems	M. SANTAMBROGIO
15:30	Coffee break	
16:00	Lab Tutorial: The RVI communication block (CommBlock)	K.S. MANNATUNGA