School on Fully-Programmable Systems-on-Chip for Scientific Instrumentation



2 - 9 November 2019 Guwahati, India

This School will cover key aspects of fully-programmable Systems-On-Chip (SoC) and its applications to advanced scientific instrumentation. The activity will include the 2019 Second International Conference on Advances in Electrical, Electronic, and System Engineering (ICAEESE 2019). More information at http://www.icaeese2019.org

Further information: http://indico.ictp.it/event/8733/ smr3332@ictp.it

Directors:

A. CICUTTIN, ICTP, Italy
M.L. CRESPO, ICTP, Italy
M.B.I. REAZ, University Kebangsaan Malaysia, Malaysia
B. TIRU, Gauhati University, India

Description:

The School will feature theoretical lectures, tutorials, and hands-on laboratory sessions on modern SoC development hardware platforms. It is primarily designed for researchers, teachers, and advanced students from the Asia-Pacific region who are looking to master modern FPGA-based SoC technologies for their scientific and academic activities. The School and Conference will provide participants opportunities for cross-boundary networking and collaboration, as well as a survey of trends in research in an era of borderless science.

Topics:

- Review of Fully-programmable SoC Architectures;
- Software Automation Tools;
- SoC Design Methodologies;
- Hardware/Software System Design;
 FPGA and VHDL for Modeling and Logic
- FPGA and VHDL for Modeling and Logic Synthesis;
- Embedded C Language Programming;
- Real-time data acquisition, processing and transmission;
- Laboratory Sessions for Hands On Training and Experimentation.

ICTP Scientific Contact:

M.L. CRESPO, ICTP, Italy

External Organizing Committee:

H. J. Goswami, Gauhati University, India H. K. Kalita, Gauhati University, India D. Saikia, Gauhati University, India K.K. Sarma, Gauhati University, India U. Sarma, Gauhati University, India

How to apply:

Online application: http://indico.ictp.it/event/8733/

Female scientists are encouraged to apply.

Grants:

A limited number of grants are available to support the attendance of selected participants, with priority given to participants from developing countries. There is no registration fee.

Selected participants will also enjoy the waiver of the ICAEESE 2019 registration fee.

Deadline:

1 August 2019









