

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for  
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : 6

Type : **not specified**

## **Synthesis III - Advanced VHDL**

*Tuesday, 28 November 2006 11:00 (1:00)*

### **Content**

### **Summary**

**Primary author(s) :** NIZAR ABDALLAH

**Presenter(s) :** NIZAR ABDALLAH

**Session Classification :** Synthesis III - Advanced VHDL