

Table of contents

Tuesday 12 December 2006	1
--------------------------------	---

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Tuesday 12 December 2006

CMOS Technology II & III - Adriatico Guest House Giambiagi Lecture Hall (11:00-13:00)

time	title	presenter
11:00	CMOS Technology II & III	JORGEN CHRISTIANSEN