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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Wednesday 13 December 2006

VLSI design II & III - Adriatico Guest House Giambiagi Lecture Hall (11:00-13:00)

time	title	presenter
11:00	VLSI design II & III	JORGEN CHRISTIANSEN