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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Thursday 14 December 2006

**Digital Signal Processing and Radar Applications IV - Adriatico Guest House Giambiagi
Lecture Hall (12:00-13:00)**

time	title	presenter
12:00	Digital Signal Processing and Radar Applications IV	KONSTANTIN LUKIN