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# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

**Tuesday 28 November 2006**

**Digital Design II (Sequential Elements, Mealy and Moore FSM) - Adriatico Guest House Giambiagi Lecture Hall (14:30-15:30)**

time	title	presenter
14:30	Digital Design II (Sequential Elements, Mealy and Moore FSM)	PIROUZ BAZARGAN-SABET