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# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

**Tuesday 28 November 2006**

**(LiberioTM IDE) Functional Simulation. Synthesis - Adriatico Guest House Giambiagi Lecture Hall (17:00-18:00)**

time	title	presenter
17:00	(LiberioTM IDE) Functional Simulation. Synthesis	NIZAR ABDALLAH