

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : 12

Type : **not specified**

**Digital Design III (more complex elements: RAM,
ROM, buses, pipeline concept, etc)**

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Content

Summary

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Session Classification : Digital Design III (more complex elements: RAM, ROM, buses, pipeline concept, etc)