

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : **13**

Type : **not specified**

(Liberio™ IDE) Place & Route

Wednesday, 29 November 2006 12:00 (1:00)

Content

Summary

Primary author(s) : NIZAR ABDALLAH

Presenter(s) : NIZAR ABDALLAH

Session Classification : (Liberio™ IDE) Place & Route