

Table of contents

Wednesday 29 November 2006	1
----------------------------------	---

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Wednesday 29 November 2006

Laboratory Session - Adriatico Guest House Informatics Lab. (14:30-15:30)

time	title	presenter
14:30	Laboratory Session	MARIA LIZ CRESPO