

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for  
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : 15

Type : **not specified**

## **Laboratory Session (VHDL simulation environment, simple examples)**

*Wednesday, 29 November 2006 16:00 (2:00)*

### **Content**

### **Summary**

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**Session Classification :** Laboratory Session (VHDL simulation environment, simple examples)