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# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

## Thursday 30 November 2006

**Digital arithmetic I (number representations) - Adriatico Guest House Giambiagi Lecture Hall (12:00-13:00)**

time	title	presenter
12:00	Digital arithmetic I (number representations)	PIROUZ BAZARGAN-SABET