

Table of contents

Thursday 30 November 2006	1
---------------------------------	---

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Thursday 30 November 2006

Laboratory Session - Adriatico Guest House Informatics Lab. (16:00-18:00)

time	title	presenter
16:00	Laboratory Session	MARIA LIZ CRESPO