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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Monday 27 November 2006

Course Overview, Introduction to FPGA Synthesis I, Introduction to VHDL (14:15-15:15)

| time | title | presenter |
|-------|---|----------------|
| 14:15 | Course Overview, Introduction to FPGA Synthesis I, Introduction to VHDL | NIZAR ABDALLAH |