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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

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(Liberio™ IDE) Introduction to Actel Products. Libero IDE Overview and Design Flow
(15:45-16:45)

time	title	presenter
15:45	(Liberio™ IDE) Introduction to Actel Products. Libero IDE Overview and Design Flow	NIZAR ABDALLAH