

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : **37**

Type : **not specified**

Selected topics on FPGA design I

Wednesday, 6 December 2006 11:00 (1:00)

Content

Summary

Primary author(s) : ANDRES CICUTTIN

Presenter(s) : ANDRES CICUTTIN

Session Classification : Selected topics on FPGA design I