Table of contents

i

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Wednesday 06 December 2006

Hardware Description of the FPGA Development Platform - Adriatico Guest House Informatics Lab. (14:30-15:30)

time	title	presenter
14:30	Hardware Description of the FPGA Development Platform	ALEXANDER SHAPIRO

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr   Wednesday 06 December 2006
1780) / Programme

Page 1