

Table of contents

Tuesday 28 November 2006	1
--------------------------------	---

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Tuesday 28 November 2006

**Synthesis II - Introduction to VHDL - Adriatico Guest House Giambiagi Lecture Hall
(09:30-10:30)**

time	title	presenter
09:30	Synthesis II - Introduction to VHDL	NIZAR ABDALLAH