

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : **48**

Type : **not specified**

Laboratory projects: Proposals and discussion (cont)

Friday, 8 December 2006 14:30 (1:00)

Content

Summary

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Session Classification : Laboratory projects: Proposals and discussion (cont)