

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : **52**

Type : **not specified**

Laboratory projects: Proposals and discussion (cont)

Monday, 11 December 2006 14:30 (1:00)

Content

Summary

Primary author(s) : MARIA LIZ CRESPO

Presenter(s) : MARIA LIZ CRESPO

Session Classification : Laboratory projects: Proposals and discussion (cont)