



# Table of contents

Monday 11 December 2006 .....	1
-------------------------------	---

# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

**Monday 11 December 2006**

**Laboratory projects: Proposals and discussion (cont) - Adriatico Guest House Giambiagi  
Lecture Hall (16:00-17:00)**

time	title	presenter
16:00	Laboratory projects: Proposals and discussion (cont)	MARIA LIZ CRESPO