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# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

## Monday 11 December 2006

### Laboratory projects: Proposals and discussion (cont)

- Adriatico Guest House Giambiagi Lecture Hall (17:00-19:00)

time	title	presenter
17:00	Laboratory projects: Proposals and discussion (cont)	MARIA LIZ CRESPO