

**First ICTP Regional Microelectronics Workshop and Training on VHDL for  
Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)**

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## **Altera-MDEC FPGA Education Launch**

*Monday, 16 June 2008 09:30 (0:45)*

**Content**

**Summary**

**Session Classification :** Altera-MDEC FPGA Education Launch