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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Monday 16 June 2008

Introduction to FPGA Synthesis, Introduction to VHDL. (17:00-18:00)

time	title	presenter
17:00	Introduction to FPGA Synthesis, Introduction to VHDL.	NIZAR ABDALLAH