## First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Contribution ID: 60 Type: not specified

## Models, Mechanics, and Opportunities for Collaborative Engineering and Design in the New Cyber Age. I

Tuesday, 1 July 2008 09:30 (1:00)

Content

**Summary** 

Primary author(s): KHAN JAVED IQBAL

Presenter(s): KHAN JAVED IQBAL

Session Classification: Models, Mechanics, and Opportunities for Collaborative Engineering and

Design in the New Cyber Age. I