

Table of contents

Tuesday 01 July 2008	1
----------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 01 July 2008

Reconfigurable Virtual Instrumentation (RVI) based on FPGA (12:00-13:00)

time	title	presenter
12:00	Reconfigurable Virtual Instrumentation (RVI) based on FPGA	ANDRES CICUTTIN