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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Monday 16 June 2008

Introduction to Digital Design (Boolean logic) (18:00-19:00)

time	title	presenter
18:00	Introduction to Digital Design (Boolean logic)	PIROUZ BAZARGAN-SABET