

Table of contents

Monday 07 July 2008	1
---------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Monday 07 July 2008

Laboratory Session. Introduction to designing FPGAs with Quartus II and Altera Development Kits (15:00-16:00)

time	title	presenter
15:00	Laboratory Session. Introduction to designing FPGAs with Quartus II and Altera Development Kits	KING KEONG WANG, THIAM SIN LAI