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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Monday 07 July 2008

Laboratory Session. Introduction to designing FPGAs with Quartus II and Altera Development Kits (16:30-18:00)

time	title	presenter
16:30	Laboratory Session. Introduction to designing FPGAs with Quartus II and Altera Development Kits	KING KEONG WANG, THIAM SIN LAI