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# **First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)**

## **Wednesday 09 July 2008**

### **An Overview of Microprocessor Architecture & Implementation of a Large Bus Size VLIW Microprocessor on FPGA. (11:00-12:00)**

time	title	presenter
11:00	An Overview of Microprocessor Architecture & Implementation of a Large Bus Size VLIW Microprocessor on FPGA.	