

Table of contents

Wednesday 09 July 2008	1
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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Wednesday 09 July 2008

Laboratory Session. Digital Design and Implementation on FPGA Using Mentor Graphics\' FPGA Advantage (16:30-18:00)

time	title	presenter
16:30	Laboratory Session. Digital Design and Implementation on FPGA Using Mentor Graphics\' FPGA Advantage	CHOONG YEE LEE