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# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Friday 11 July 2008

### Closing Ceremony (UA113, Sport Complex)

**(11:00-13:00)**

| time  | title                                   | presenter |
|-------|---|-----------|
| 11:00 | Closing Ceremony (UA113, Sport Complex) |           |