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J J	

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Friday 11 July 2008

<u>Certificates of Participation (DK E) (15:00-16:00)</u>

time title	presenter
15:00 Certificates of Participation (DK E)	