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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 17 June 2008

Digital Design I (combinatorial elements) (16:30-17:30)

time	title	presenter
16:30	Digital Design I (combinatorial elements)	PIROUZ BAZARGAN-SABET