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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Thursday 19 June 2008

Digital arithmetic I (number representations) (11:00-12:00)

| time | title | presenter |
|-------|---|-----------------------|
| 11:00 | Digital arithmetic I (number representations) | PIROUZ BAZARGAN-SABET |