



# Table of contents

Monday 23 June 2008 .....	1
---------------------------	---

# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Monday 23 June 2008

### CMOS Technology I (16:30-17:30)

time	title	presenter
16:30	CMOS Technology I	PAULO MOREIRA