

Table of contents

Tuesday 24 June 2008	1
----------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 24 June 2008

CMOS Technology II (12:00-13:00)

time	title	presenter
12:00	CMOS Technology II	PAULO MOREIRA