

**First ICTP Regional Microelectronics Workshop and Training on VHDL for
Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)**

Contribution ID : **38**

Type : **not specified**

VLSI Design I

Tuesday, 24 June 2008 15:00 (1:00)

Content

Summary

Primary author(s) : PAULO MOREIRA

Presenter(s) : PAULO MOREIRA

Session Classification : VLSI Design I