Table of contents

Fuesday 24 June 2008	Γuesday 24 June 2008		
----------------------	----------------------	--	--

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 24 June 2008

VLSI Design I (15:00-16:00)

time title	presenter
15:00 VLSI Design I	PAULO MOREIRA