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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 24 June 2008

Laboratory Session. Hardware Description of the FPGA Development Platform. FPGA Implementation Example. (17:30-20:00)

time	title	presenter
17:30	Laboratory Session. Hardware Description of the FPGA Development Platform. FPGA Implementation Example.	CARLOS SOSA PAEZ