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# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Wednesday 25 June 2008

### Advance FPGA applications. A case study in HEP experiments (contd) (15:00-16:00)

| time  | title  | presenter       |
|-------|--|-----------------|
| 15:00 | Advance FPGA applications. A case study in HEP experiments (contd) | ALEXANDER KLUGE |