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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

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Laboratory Session. Hardware Description of the FPGA Development Platform. FPGA Implementation Example. (cont) (16:30-19:00)

time	title	presenter
16:30	Laboratory Session. Hardware Description of the FPGA Development Platform. FPGA Implementation Example. (cont)	CARLOS SOSA PAEZ