

Table of contents

Thursday 26 June 2008	1
-----------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Thursday 26 June 2008

Fourier Theory II (12:00-13:00)

time	title	presenter
12:00	Fourier Theory II	MARCELO MAGNASCO