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# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Friday 27 June 2008

### Laboratory Session. Implementation in the FPGA Development Platform. VHDL Package and Structural Design. (15:00-16:00)

| time  | title  | presenter        |
|-------|--|------------------|
| 15:00 | Laboratory Session. Implementation in the FPGA Development Platform. VHDL Package and Structural Design. | MARIA LIZ CRESPO |