

Table of contents

Monday 30 June 2008	1
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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Monday 30 June 2008

Selected topics on Logic Synthesis and FPGA Debugging (cont.) (11:00-12:00)

time	title	presenter
11:00	Selected topics on Logic Synthesis and FPGA Debugging (cont.)	ANDRES CICUTTIN