

Laboratory Session. Implementation in the FPGA Development Platform. Bidirectional Parallel Port Communication.

Monday, 30 June 2008 12:00 (1:00)

Content

Summary

Primary author(s) : MARIA LIZ CRESPO

Presenter(s) : MARIA LIZ CRESPO

Session Classification : Laboratory Session. Implementation in the FPGA Development Platform. Bidirectional Parallel Port Communication.